

**SHAID SMARAK COLLEGE**

Kirtipur, Kathmandu

*Lab no : 2 of Digital logics*

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1st semester

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**NAND Implementation**

The implementation of a Boolean function with NAND gates requires that the function be simplified in the SOP. To see the relationship between a SOP expression and its equivalent NAND implementation. The diagrams below are equivalent and implement the function: F=AB+CD+E.